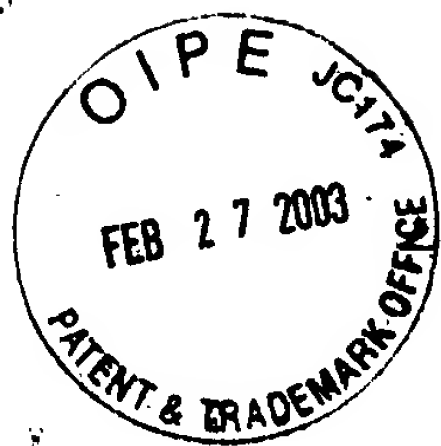


AF/2814
#19



BOX AF
REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2814

PATENT
0033-0694P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:	SUMIKAWA, et al.	Conf.:	2465
Appl. No.:	09/782,180	Group:	2814
Filed:	February 14, 2001	Examiner:	D. NGUYEN
For:	SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME		

LETTER REGARDING INTERVIEW OF FEBRUARY 25, 2003

Assistant Commissioner for Patents
Washington, DC 20231

February 27, 2003

Sir:

The Interview Summary given to the undersigned correctly summarizes the interview. Additionally, as understood from the position of the Primary Examiner, a semiconductor of the reference would be very small in thickness, and accordingly, the tape of the reference would inherently provide reinforcement. The undersigned did state there was no evidence of this, and that inherency (implicit) cannot be based on probability or possibilities.

RECEIVED
MAR - 3 2003
JIC 2800 MAIL ROOM

CONCLUSION

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Elliot A. Goldberg (Reg. No. 33,347) at the telephone number of (703) 205-8000, in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully Submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By: 

Charles Gorenstein

Reg. No. 29,271

P.O. Box 747

Falls Church, VA 22040-0747

(703) 205-8000

CG/EAG/kss